

*Fig. 1*

FIG. 2A is a schematic diagram of a first embodiment of a device. The device includes a first input terminal (B1) and a first output terminal (B2). The device also includes a second input terminal (B1) and a second output terminal (B2). The device is configured to receive a signal at the first input terminal (B1) and output a signal at the first output terminal (B2). The device is also configured to receive a signal at the second input terminal (B1) and output a signal at the second output terminal (B2).

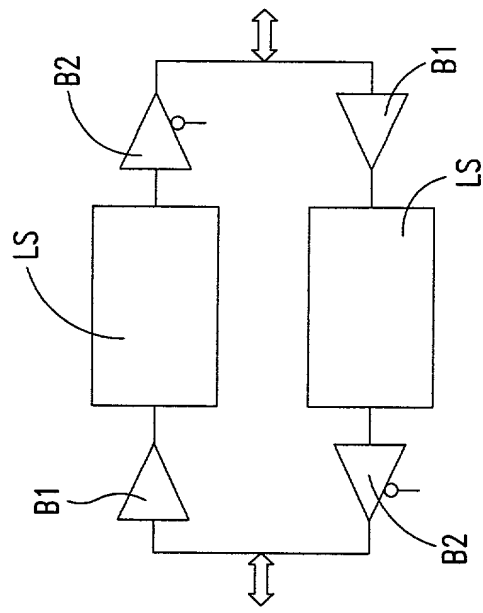


Fig. 2A

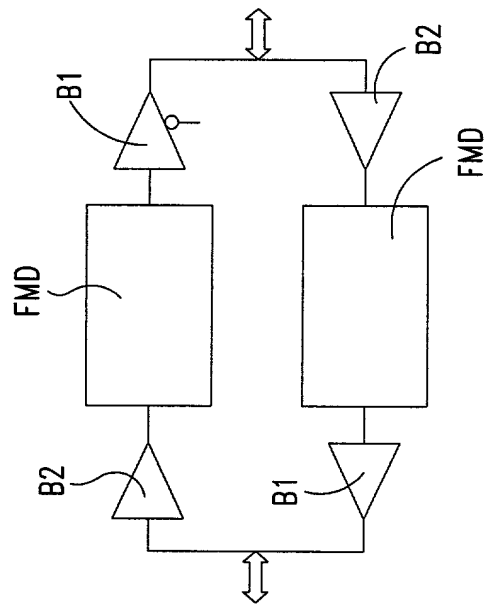


Fig. 2B

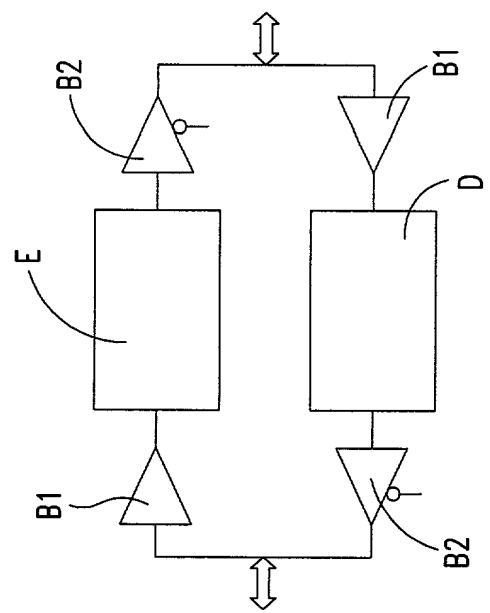


Fig. 2C

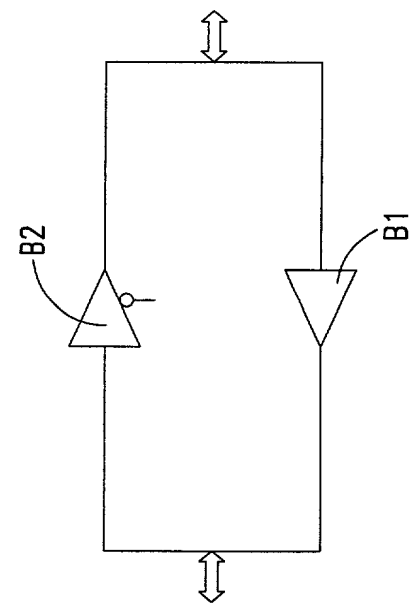
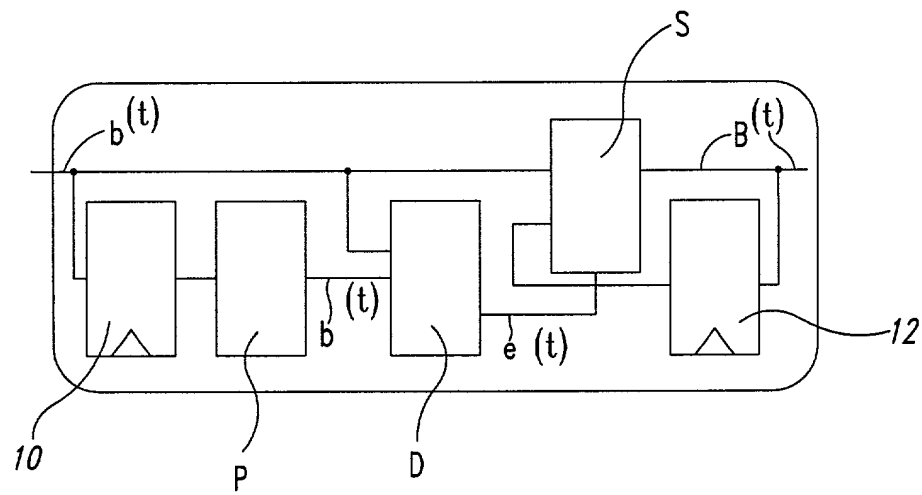
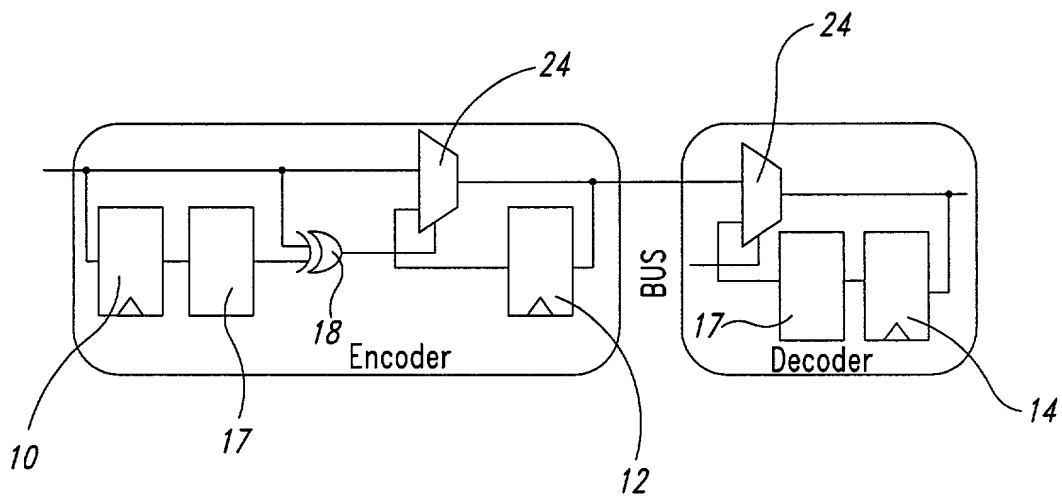


Fig. 2D



*Fig. 3*



*Fig. 4*

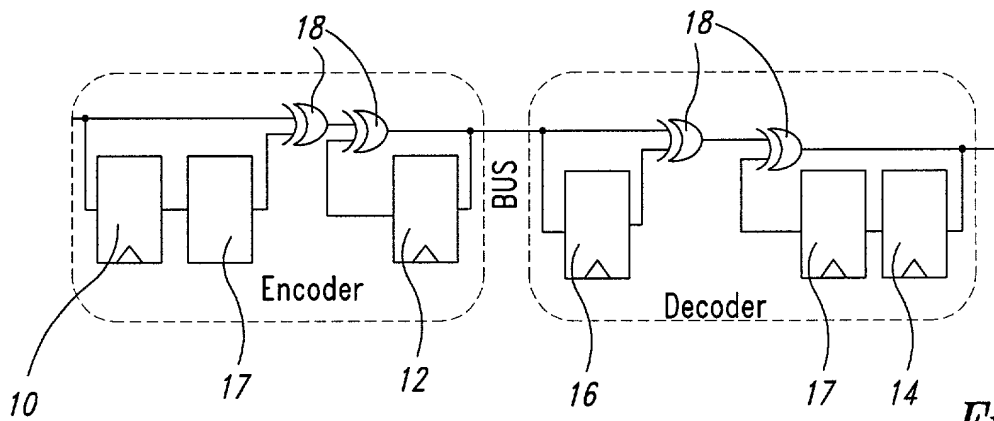


Fig. 5

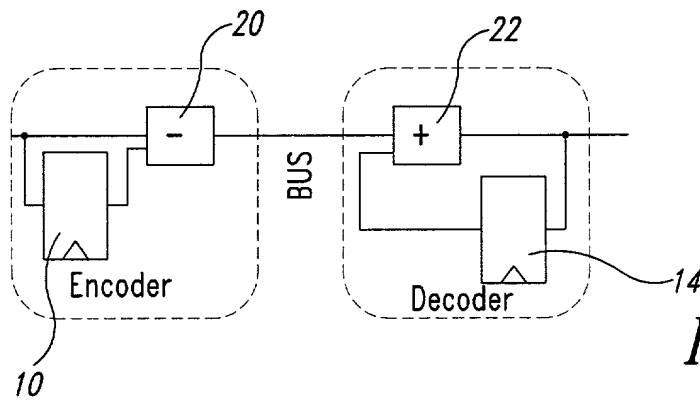


Fig. 6

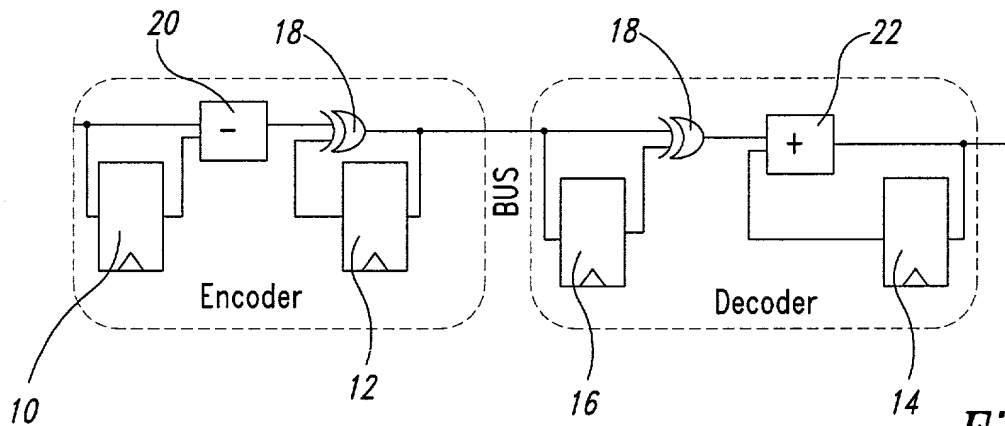
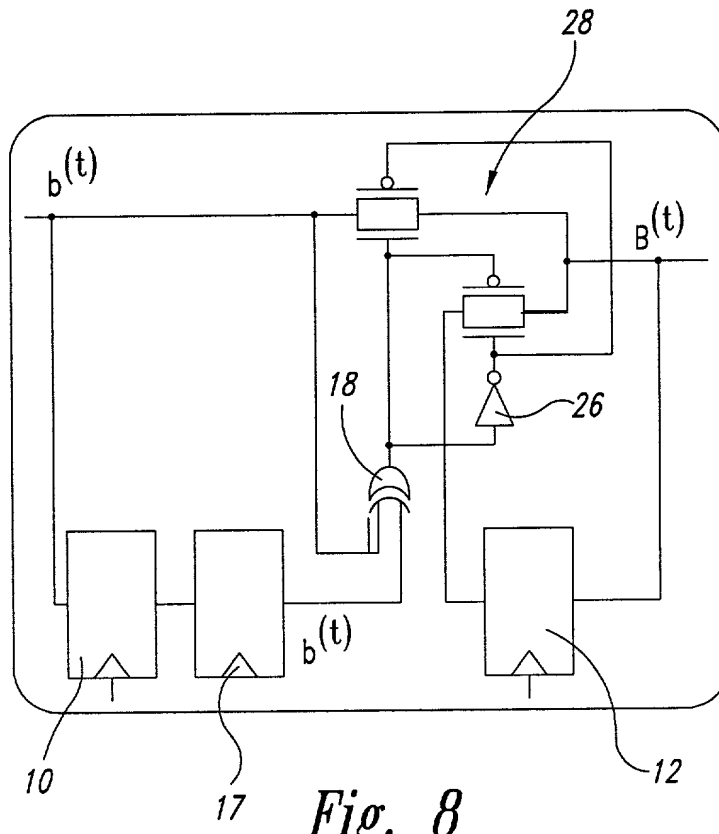
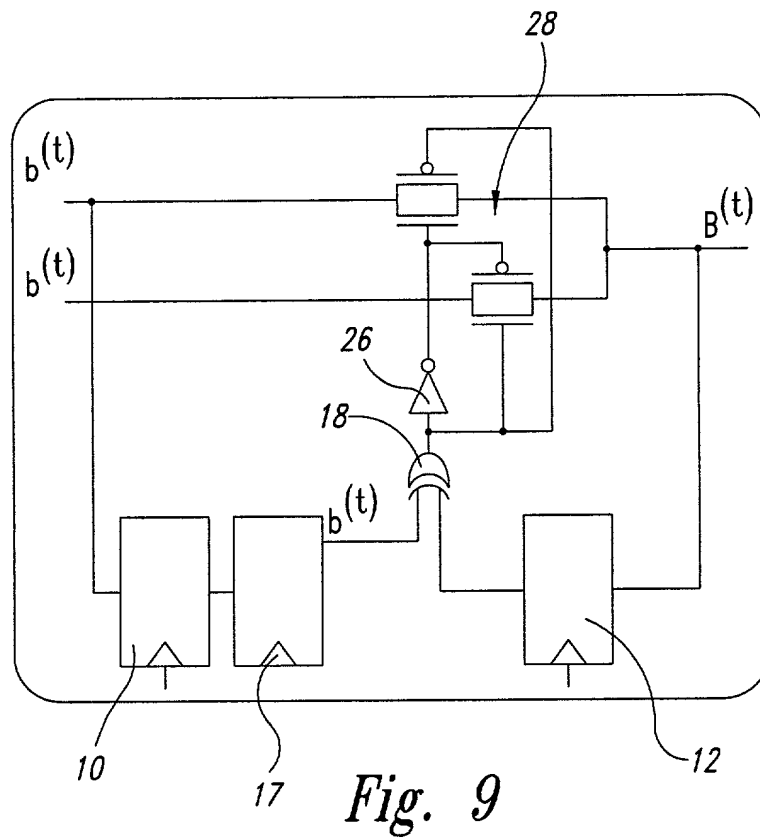


Fig. 7



*Fig. 8*



*Fig. 9*